



PATENT

Attorney's Docket No. MP0117

#3

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Pannell, Donald et al.

Group Art Unit: Not Yet Assigned

Serial No.: 10/071417

Examiner: Not Yet Assigned

Filed: February 6, 2002

RECEIVED

Title: Quality of Service Queueing System for a Network Switch

APR 23 2002

Technology Center 2600

CERTIFICATE OF MAILING

I hereby certify that this correspondence, and the documents attached hereto, are being deposited with the United States Postal Service as First Class mail in an envelope addressed to Commissioner for Patents, Washington, D.C. 20231 on this date.

Date:

4/8/02

Suzanne M. Forno
Suzanne M. Forno

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, and in accordance with the practice under 37 C.F.R. § 1.97 and § 1.98, the Examiner's attention is directed to the documents listed on the enclosed Form PTO-1449. Copies of the listed information are also enclosed.

This Information Disclosure Statement is being filed within three months of the U.S. filing date or before the mailing date of a first Office Action on the merits. No certification or fee is required (37 CFR §1.97(b)).

CONCLUSION

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any over-payment to deposit account 50-1236.

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be initialed and returned indicating that such information has been considered.

Respectfully submitted,



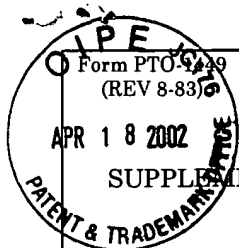
Eric B. Janofsky
Attorney for Applicants
Registration No. 30759

Please address all correspondence to:

Eric B. Janofsky
General Patent Counsel
Marvell Semiconductor, Inc.
700 First Avenue, Mail Stop 509
Sunnyvale, CA 94089
General Telephone Number (408) 222-2500
Facsimile (408) 752-9034
Email: sforno@marvell.com
Customer No. 23624

Date:





Form PTO-609
(REV 8-83)

DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

SERIAL NO. 10/071417

ATTY DOCKET NO.
MP0117

APPLICANT

Pannell, Donald et al.

SUPPLEMENTAL INFORMATION DISCLOSURE
CITATION

(USE SEVERAL SHEETS IF NECESSARY)

PAGE 1 OF 1

FILING DATE

February 6, 2002

GROUP: Not Yet
Assigned

U.S. PATENT DOCUMENTS

E.I.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLAS S	FILING DATE
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					

RECEIVED

APR 23 2002

Technology Center 2600

FOREIGN PATENT DOCUMENTS

E.I.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLAS S	TRANS- LATION
	AL					
	AM					
	AN					
	AO					
	AP					
	AQ					

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

AR	Cyriel Minkenberg and Ton Engbersen, "A Combined Input and Output Queued Packet-Switched System Based on PRIZMA Switch-on-a-Chip Technology," IEEE Communications Magazine, December 2000, pp. 70-77
AS	H. Jonathan Chao, Cheuk H. Lam, Eiji Oki, "Broadband Packet Switching Technologies: A Practical Guide to ATM Switches and IP Routers," New York, New York, 2001, pp. 321-324.
AT	
EXAMINER	
DATE CONSIDERED	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.